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Applicant(s): Hariprakash Govindarajalu

Serial No.: 10/807.483

For: ERROR DETECTING ARITHMETIC CIRCUITS USING

HEXADECIMAL DIGITAL ROOTS

Filing Date: March 23, 2004 Examiner: David H. Malzahn

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MAIL STOP AMENDMENT

Commissioner for Patents PO Box 1450 Alexandria, Virginia 22313

Sir:

AMENDMENT

In response to the Office Action mailed on <u>September 11, 2007</u>, please amend the above-identified Application as follows: